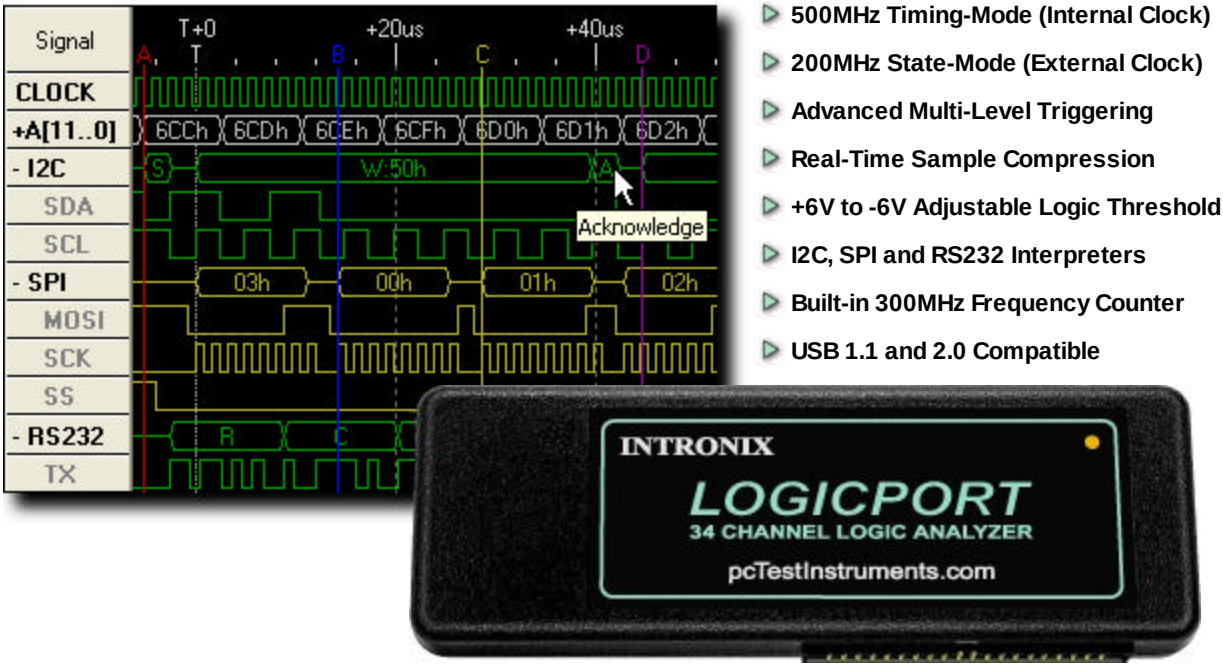
INTRONIX PC-Based Test and Measurement

34 CHANNEL LA1034 LOGICPORT LOGIC ANALYZER



- 500MHz Timing-Mode (Internal Clock)
- 200MHz State-Mode (External Clock)
- Advanced Multi-Level Triggering
- Real-Time Sample Compression
- +6V to -6V Adjustable Logic Threshold
- I2C, SPI and RS232 Interpreters
- Built-in 300MHz Frequency Counter
- USB 1.1 and 2.0 Compatible

The LogicPort provides 34 channels sampled at 500MHz. This includes two state-clock inputs which function as normal sampled channels in timing-mode. The LogicPort is controlled and powered via your PC's USB 1.1 or 2.0 port for the ultimate in convenience and portability.

Features include multi-level trigger capability, qualified state-mode sampling with adjustable setup/hold window, selectable logic sense and threshold, built-in interpreters for I2C, SPI and RS232 protocols, and much more.

The LogicPort's advanced trigger features allow it to trigger on simple or complex sequences of edges, patterns and bus numerical ranges, as well as on specified pattern, range or pulse durations.

You'll find the LogicPort's software to be feature-rich, yet simple to use. Visit www.pcTestInstruments.com to try the free software for yourself. The installation includes real-world examples of actual acquired data. See a 125MHz SDRAM interface sampled at 500MHz, the outputs of a 100MHz A/D converter sampled in state mode, interpreted I2C, SPI and RS232 data streams, activity on a typical Intel processor bus and more.

Compare the LogicPort's specifications to those of logic analyzers costing thousands more. With its high speed sampling, advanced trigger capabilities and real-time sample compression, the LogicPort gives the big guys a run for their money!

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Intronix LA1034 LogicPort Specifications and Characteristics

Sampled channels: 34

Timing mode sample rate: 1KHz to 500MHz (uses internal clock)

State mode sample rate: 0 to 200MHz (clock provided by circuit under test)

Sample buffer: 34 x 2048 samples

Maximum sample compression: 2^33 to 1 (sample rates to 200MHz)

Trigger sequencer: 250MHz max, 4ns minimum pulse width

Trigger event counters: 20 bits, range 1 to 1,048,576 for each trigger level

Input impedance: 200K Ohms, paralleled by <5pF

Frequency counter range: 300MHz with 10Hz resolution, 4 channels

Threshold range: adjustable +6 to -6 volts with 50mv resolution

Threshold accuracy: +/-(100mV + 5% of setting)

Channel to channel skew: 0.6ns typical, 1.0ns max

State mode Setup/Hold times: 2.0/0ns, window adjustable +/-2.5ns

Input sensitivity: 0.25Vpp @50MHz, 0.5Vpp @150MHz, 0.8Vpp @250MHz

Input dynamic range: 10 volts peak-peak

Maximum input: +/-40 volts DC, 15 volts peak-peak pulse amplitude

Timebase accuracy: +/-0.005% over full temperature range

Input Probe: Teflon insulated leads terminated with female contacts compatible with 0.025"-0.030" round or square pins.

Power supply current: 200 milliamps max (supplied by USB)

Temperature range: operating +5 to +50C, storage -10 to +65C

Minimum system requirements:

Pentium II or better (400MHz or faster recommended)

32MB available memory

20MB available disk space

800 x 600 (1024 x 768 recommended) standard DPI display

USB 1.1 or 2.0 port

The LA1034 is compatible with the following operating systems:

Windows 98

Windows 98 Second Edition

Windows ME

Windows 2000

Windows XP Home

Windows XP Professional

Windows Vista

Specifications subject to change without notice